Ofertas de Trabajo de Otras Entidades

Ofertas diversas

- Postdoctoral Research Fellow in Resource Optimisation in 5G Networks at the University of Exeter (UK). Received: 22/11/2018, closing date: 07/01/2019.

This EPSRC funded post is available immediately on a two year fixed term basis to support the work of Dr. Yulei Wu.

The University of Exeter is a member of the prestigious Russell Group of research-intensive universities. We combine world-class teaching with world-class research, and have achieved a Gold rating in the Teaching Excellence Framework Award 2017. The University of Exeter has over 22,000 students and 4600 staff from 180 different countries and has been rated the Whatuni2017 International Student Choice. Our research focuses on some of the most fundamental issues facing humankind today, with 98% of our research rated as being of international quality in the 2014 Research Excellence Framework. We encourage proactive engagement with industry, business and community partners to enhance the impact of research and education and improve the employability of our students.

More details can be found https://jobs.exeter.ac.uk/hrp_webrecruitment/wrd/run/ETREC107GF.open?VACANCY_ID=112157LudQ8&VVID=3817591jNg&LANG=USA. If you are interested in this position, please get in touch or forward it to anyone who might be interested.

The closing date for applications is midnight (GMT) on 07 January 2019.

- Post-Doctoral Researcher Position in Cognitive Cybersecurity at the Technical University of Berlin (Germany). Received: 19/11/2018.

GT-ARC gGmbH in Berlin, Germany, is currently seeking a post-doctoral researcher as the team leader of its Cybersecurity competence center.

GT-ARC is an affiliated research institute of the Technical University of Berlin, which is also co-located and works closely with the Distributed Artificial Intelligence Laboratory (DAI-Labor) at TU Berlin, under the leadership of Prof. Dr. Sahin Albayrak. The advertised position will have active involvement in research and development activities at both GT-ARC and DAI-Labor. Undertaking teaching duties at TU Berlin is also possible.

The selected candidate will lead a team of four to five researchers (Ph.D. students) at GT-ARC /TU Berlin and should be able to conduct both theoretical and applied research in the development and application of novel AI-driven cybersecurity solutions.

More details on this and two other open positions can be found at http://www.gt-arc.com/jobs/, together with the application guidelines.

- Post-Doctoral Position in 5G at University of Waterloo (Canada). Received: 14/11/2018.

Post Doctoral position in the broad area of 5G networking. The position is in the Department of Electrical and Computer Engineering at University of Waterloo, Canada.

A PhD. in Electrical and Computer Engineering or Computer Science is required. Strong analytical skills and strong communications skills in English are also required.

Applicants should submit a cover letter, a resume, and the names of two references via email -- LOGIN=6fc013333e343533fe79553431fe3930@waterloo[dot]ca . Please note that the topic line should be 5G POSTDOC.

This position is for one year and may be renewed by mutual consent.


Key words: Performance Evaluation, Resource Management, Network Economics, 5G, Optimisation.

Deadline for applications: November 29th, 2018

Context

In 5G systems, “network slicing” implements a logically-isolated network partitioning: a so-called “slice” represents a unit of combined programmable resource composed of network, computation and storage resources. The concept was originally proposed for overlay networks, but is now being discussed for Radio Access Networks (RANs) and core networks thanks to the increased programmability of RAN and core network elements. These recent evolutions are due to huge effort devoted to the development of software for network virtualization systems and related management tools based on virtualization in the last 5 years. In particular, research in Network Functions Virtualization (NFV), Mobile Edge Computing (MEC) and Software Defined Radio (SDR) have contributed to lay the pavement for flexible radio design at the physical and MAC layers. These technology advances are revolutionizing the way telecommunication networks are operated. However, they open also a number of challenges of both of algorithmic and of fundamental nature.

In the last few years we have seen unprecedented levels of “cooperation” (combining cooperation and competition) as mobile operators embark on various models of network sharing to reduce the deployment, management and energy costs they face. They do so in order to meet demand amidst the constantly decreasing revenues. To this respect, slicing has also been imagined as a principle for simplifying and optimizing network sharing between operators. In this context, one slice would be associated with an operator of a virtual network deployed on another operator’s infrastructure or even on a shared infrastructure.

These two slicing dimensions are linked, as the second one enables the first one, working at different decision-making time-scales. It is worth noting that the project will integrate these dimensions for slicing within a unified framework. The performance of the scheme is to be evaluated for a dense urban environment. Doing so, the advantage is that of integrating different services related to smart cities and vertical industries (e.g., factory automation), along with mobile broadband connectivity offerings. In the Smart City scenario, moreover, the access coverage between different operators typically has an high degree of geographical overlap, increasing the value for resource and infrastructure sharing.

Objectives and expected contributions:

Given the diversity in the requirements which is expected by the industry in 5G networks, new and more flexible approaches for resources management are needed. They will provide support for increased business flexibility and foster further future innovations, accelerate vertical and horizontal integration and simplify the management of large and complex sets of use cases and business models. New business models, in turn, will help new entrant market players to develop and analyze the perspectives of potential win-win strategies based on the developed solutions. This thesis will explore the means to deliver such achievements to the market with emphasis in the quantification of benefits, especially in terms of revenues and margins possible with such resources allocation schemes.

The main objective for this PhD position is to contribute to address these key objectives:

- A resource allocation framework for slices, integrating heterogeneous QoS requirements and spanning on multiple resources including radio, backhauling/fronthauling and processing resources in the RAN.
- A slice orchestration architecture including strategic provisioning and dynamic re-optimizations algorithms and their integration with NFV and SDN strata.
- A business layer for slicing in 5G, enabling win-win configurations between players from the telecommunications industry and vertical applications, ensuring that 5G services are commercially viable and gain the acceptance of the market.

Interested candidates can contact

Rachid Elazouzi -LOGIN–9d55c30bcf983526345d9abb1b57de79duniv-avignon[dot]fr or
Majed Haddad : -LOGIN–0e745c11d483b8974408e5bd2e81univ-avignon[dot]fr or
Francesco de Pellegrini –LOGIN–4abc7c1335f7c2b7e9055d012768euniv-avignon[dot]fr

for further information on the project and work place.

- ProfessorPositions -Faculty Positions in Higher Education. Received: 12/11/2018.

ProfessorPositions

Computer Science

- Assistant Professors (tenure-track) or Associate Professors in Computer Science, Aarhus University (Denmark)
- Post-Doctoral Researcher at LERO, School of Computer Science and Information Technology, University College Cork (Ireland)
- Senior Post-Doctoral Researcher at LERO, School of Computer Science and Information Technology, University College Cork (Ireland)
- P.M. Thapar Chair, Professor of Computer Science, Trinity College Dublin (Ireland)
- Postdoctoral Fellowship in Computing Science, University of Groningen (Netherlands)
- Postdoctoral Fellowship in Artificial Intelligence, University of Groningen (Netherlands)
- Postdoctoral Fellowship in Human Computer Interaction, University of Groningen (Netherlands)
- University Leader in IT Operations, Network and Security, Norwegian University of Science and Technology (Norway)
- PhD position in Mining Social Media Networks, Norwegian University of Science and Technology (Norway)
- PhD position in Mining Social Media Networks, Norwegian University of Science and Technology (Norway)
- University Lecturer in Information Security and IT Management, Norwegian University of Science and Technology (Norway)
- Tenure Track Associate Professor in Software, University of Bergen (Norway)
- PhD student in information security, Lund University (Sweden)
- Postdoctoral in Information Security, Lund University (Sweden)
- University lecturer in informatics, especially design of information systems, Lund University (Sweden)
- Assistant or untenured Associate Professor at Department of Computer Science, Stanford University (USA)

- Oferta de trabajo en el ICM-CSIC, Barcelona (Spain). Received: 08/11/2018, deadline: 30/11/2018.
PUETO: PROGRAMADOR

- DESCRIPCIÓN:
  El proyecto COSMO (Corrientes Oceánicas y Seguridad en el Medio Marítimo, CTM2016-79474-R) tiene entre sus objetivos elaborar una base de datos de objetos derivantes en el medio marino (fundamentalmente flotadores a la deriva) en áreas circundantes a la Península Ibérica. El objetivo es realizar un inventario de datos existentes, principalmente en los grupos de investigación marina del CSIC y la Agencia Estatal de Salvamento Marítimo (ASEMAR), diseñar la base de datos y el software necesario para su consulta y actualización.

- TAREAS A REALIZAR:
  • Diseño de una base de datos para albergar datos de objetos derivantes a partir de registros disponibles en instituciones nacionales e internacionales en áreas geográficas circundantes a la Península Ibérica. Esta tarea puede requerir la necesidad de realizar alguna estancia corta en parte de los centros nacionales suministradores de datos.
  • Uniformización de formatos y codificación de los datos en base a los estándares definidos en el proyecto COSMO.
  • Elaboración del software necesario para acceso y actualización de los mismos.
  • Elaboración de la documentación de la base de datos y su software relacionado.

- LUGAR DE TRABAJO:
  El trabajo se realizará en el Instituto de Ciencias del Mar (ICM-CSIC) en Barcelona, aunque se podría requerir alguna estancia corta en otro centro nacional colaborador

- REQUISITOS:
  • Licenciatura/Ingeniería/Grado en Informática, Matemáticas o Físicas
  • Experiencia imprescindible en:
  lenguajes de programación de base de datos (SQL)
  programación en Python
  • Mérito preferente el estar familiarizado con formatos de intercambio de datos (netCDF, JSON).

El candidato seleccionado deberá incorporarse a principio del 2019 como muy tarde el 1 de febrero de 2019. El salario será entre 1600-1900 euros brutos al mes (según convenio). La duración del contrato es de 6 meses (prorrogable a 1 año)

Los interesados pueden enviar su currículum (y solicitar más información) a Emilio García Ladona (LOGIN--a18d6d8019f5b052d2661d1802060771cm[dot]cis[dot]es) o a Joaquim Ballabrera (LOGIN--72a674a23a817f4036e639f5f5cfe18531cm[dot]cis[dot]es) antes del 30 de noviembre de 2018. Solicitudes fuera de este plazo solamente se considerarán si no se ha cubierto el puesto.

  • Postdoc Position in Distributed Systems and Networks at the University of Lisbon (Portugal). Received: 06/11/2018, deadline: 28/11/2018.
  Position

You will be working on a three-year project funded by the National Science Foundation (FCT)
The goal of the project is to address the reproducibility problems in the experimental evaluation of distributed systems and networks. Experiment reproducibility is a core tenet of the scientific method but unfortunately the increasing complexity of the systems we build, deploy and evaluate makes it difficult to reproduce -and understand -experimental results.

The focus of this position is to research, design and develop the methods, models and tools that enable: i) a better experimental evaluation process and ii) an experimenter to pinpoint, or induce, pathological scenarios in the system under study.

This includes the performance and correctness aspects of large-scale systems and networks evaluation when subject to different conditions, workloads and fault patterns.

For more information please contact Professor Miguel Matos (--LOGIN--82a06d5f0d072918bee4eb70a95dc4tecnicodatolisboa/datapot).

We seek
We are looking for a researcher with an independent mind who is willing to work on the above topics and collaborate with a team of other researchers and students.
As background, we seek candidates with practical experience in the “system's side” of designing and building distributed systems and networks.

The candidate should be proficient in written and spoken English.

As a research outcome, we expect publications in top-tier venues and (prototype) tools.

We offer
- One Postdoc position for 30 months
- A stimulating scientific environment with English as a working language
- Full status as an employee including pension and health care benefits
- Longer term tenure-track positions are regularly open at Instituto Superior Técnico, University of Lisbon, which offer additional opportunities for those interested in pursuing a career in Lisbon
- Possibility to work with students at master's and Ph.D. level

Further information
-Distributed System group -http://www.gsd.inesc-id.pt/gsd-members/
-Professor Miguel Matos -http://www.gsd.inesc-id.pt/~mm/
-Project Web Page -http://angainor.science/

Application
To apply visit
(Short link: https://is.gd/INduKn)
Deadline: November 28, 2018
About INESC-ID
INESC-ID is a research laboratory of Instituto Superior Técnico (IST - Universidade de Lisboa) in the areas of Electrical and Computer Engineering and Computer Science. INESC-ID's researchers regularly collaborate with the leading groups worldwide and publish their results in the most competitive venues. Several of its researchers have been awarded some of the most important recognitions in their fields, namely two IEEE Fellows, two ACM Distinguished Members, and one ERC Grant. INESC-ID is highly involved not only in EU projects, networks of excellence and advanced training programs but also in partnerships with major universities in Europe and the US.
https://www.inesc-id.pt/about-us/

- Research Associate Position in Electronic Engineering -Sensor Fusion at the University of Bedfordshire and Harrod Sport Ltd, Lowestoft, Suffolk (UK). Received: 02/11/2018, closing date: 03/12/2018.

Research Associate (KTP) Position in Electronic Engineering -Sensor Fusion
University of Bedfordshire and Harrod Sport Ltd hire a Research Associate under the Knowledge Transfer Partnership (KTP) program funded by Innovate UK agency in the area of integrating a sensor fusion system and developing and testing algorithms for ball localization in various sports.

Contract type: Full time, 30 months fixed-term
Salary: GBP 27,000 (depending on experience and qualifications)
Location: Lowestoft, Suffolk, UK

Harrod Sport Limited is the UK’s leading manufacturer of football goal posts, hockey goals, tennis posts and general sports ground equipment.

In partnership with the University of Bedfordshire (under the Knowledge Transfer Partnership scheme), Harrod Sport Limited are looking for an enthusiastic recent graduate (max 5 years since graduation) to help deliver a project and to take a new innovative product to market. This is fantastic opportunity for an ambitious graduate to launch a career in industry with the support of company and academic mentors over a period of 30 months.

You will be responsible for the project management, involving the development of an optimised combination of sensors (including ultrasonic, infrared, cameras and laser sensors) that will estimate in real-time the data related to the ball velocity and interception point location between the ball and football goal bar plane. You will also be responsible for developing a microcontroller system to support the sensors and to enable real-time wireless data transmission to a mobile device user application.

Essential Criteria
- BEng or MSc in Electronic/Electrical Engineering or in subjects very closely related to this
- Knowledge of a broad range of microcontroller and sensor technologies
- Strong knowledge of electronics assembly processes and test methodologies
- Understanding of software design for microcontrollers
- Excellent verbal and written communication skills with the ability to understand complex technical information and convey this into easily understood instructions
- Ability to work to deadlines while demonstrating a track record for innovation and creativity
- Strong command of English language with the ability to accurately interpret complex standards
- The ability to contribute to a team effort whilst being a self-starter, highly motivated, and willing to take the project lead
- The ability to demonstrate a logical approach to problem solving and methodical, investigate-oriented and inquisitive mind

Desirable Criteria
- Demonstrable experience in developing electronic systems
- Demonstrable experience in previous assembly of sensor and microprocessor systems
- Demonstrable experience in optimised algorithm development and the field of signal processing
- Previous work in teams on problem-solving in electronic engineering

Interested applicants should apply via the University of Bedfordshire vacancy page (https://www.beds.ac.uk/jobs) using the job reference BEDS1159 by 3rd December. Any question should be directed to Samantha Griffin (–LOGIN→f0c3714553a648883ad498a7e168d24beds[dot]jac[dot]uk ) or Dr Vladan Velisavljevic (–LOGIN→4ccdf7e5b7903c64fc422a1b08ce2b3beds[dot]jac[dot]uk ).

- PhD Assistant Researcher in Networks Architectures and Protocols at Instituto of Telecommunications, Aveiro (Portugal). Received: 02/11/2018, deadline: 19/11/2018.

The Institute of Telecommunications (www.it.pt) in Portugal, through its project InfoCent-IoT will hire a PhD Assistant Researcher in the area of Networks Architectures and Protocols (https://www.it.pt/Groups/Index/62). The applications are open up to 19/11/2018. The relevant information to apply is available in the following link: https://www.it.pt/Positions/PostDoc/281

- Ph.D. Position Available in Passive Communication for Industrial IoT at Tsinghua University -Siemens China Scholarship (China). Received: 01/11/2018, deadline: 15/12/2018.

I. About the Ph.D. position:

There is a Ph.D. position available at the School of Software, Tsinghua University. The PhD student is expected to work in the area of “Passive Communication for Industrial IoT”, which potentially includes both analytical and experimental work in the following aspects:
1) Hardware and software design of passive wireless nodes;
2) Energy harvesting from RF signals;
3) Ultra-Low-Power (or Zero-Power) sensing and perception;
4) Passive communication and the corresponding protocol stack design;
5) Alternative air interfaces for passive or ultra-low-power wireless communication;
6) Investigation of ultra-high-latency industrial IoT systems;

II. About the Scholarship

The Siemens China Scholarship at Tsinghua University is a full scholarship offered by Siemens Ltd., China, Corporate Technology, to support outstanding international students to pursue Ph.D. degrees. The Scholarship recipients will cooperate with Siemens to conduct research in the Ph.D.
stage. Through the study and practice in the forefront of industrial technology development, the scholarship recipients will lay the foundation for the growth of innovative talents in the industry.

* Contents and Standard of the scholarship:
  --Tuition fees: 40,000 RMB/year;
  --Living allowance (including accommodation, medical insurance, books or other materials and transportation, etc.): 160,000 RMB/year in total.

* The scholarship is awarded to the scholarship recipient for a period of four years in principle; If the Scholarship recipient completes his or her study and gets the degree in advance, the duration of the scholarship for the student shall be shortened accordingly and consistent with the actual study period. The scholarship recipient who has not been able to complete their study due to objective (exceptional) reasons within four years or will reach a better achievement in their research if the length of study is extended for a period of time may apply to extend their length of study sponsored by the scholarship after approval of study extension from the University. The scholarship is awarded on the basis of an annual review of the recipient’s performance.

III. Eligibility:

Applicants for the Scholarship should meet all the following requirements:
1. Meet the requirements of admission for international graduate students (detailed information refer to the Admission to Graduate Programs of Tsinghua University 2019 (For International Students)); Click here
2. Must appreciate Siemens Values, and be willing to work together with Siemens to carry out research work;
3. Are not offered other kinds of scholarships for study at Tsinghua University.

IV. How to apply:

The deadline for submitting the application online is December 15, 2018.

Please contact in advance with Prof. Yuan He (~LOGIN--821731ce3397411d3f5d7ce60712a9tsinghua[dot]edu[dot]cn ) with the email title “PhD Student Application ~<Name Surname>”, and attach your CV and academic transcripts.

- Two Post-doctoral Positions in Cyber Security at UNSW Sydney (Australia). Received: 31/10/2018, closing date: 02/12/2018.

Two Senior Research Associate (SRA) positions in cyber security are open at UNSW Sydney, Australia. The Senior Research Associates will work closely with Cyber Security Cooperative Research Centre (CyberCRC) to develop and engage in high quality, impactful research projects that contribute to the cyber security of Australia.

Candidates will be hired at either Level A or Level B based on experience. Level A -A $93,578 -A $100,090 per annum (plus 17% superannuation), Level B -A $105,108 -$123,908 per annum (plus 17% superannuation).

These are 3 year fixed term full-time positions.

See the following links for details and to apply:

- Two Resident Assistant and one Post Doc positions in Hardware Security and Trust /Digital IC Design at The Ohio State University (USA). Received: 31/10/2018.

The CLASS lab at ElectroScience, The Ohio State University is seeking highly skilled and motivated PhD candidates.

Project Description:
Globalization of design, fabrication, test, and assembly of electronic systems has raised serious concerns about the Security and Trustworthiness (S&T) of microelectronics. It is of paramount importance to develop protection techniques for microelectronic devices throughout their lifecycle to enable the use of commercially available technologies without compromising the S&T of the system. The candidate will be part of a research team that develops different methods to asses and enhance the S&T of system-on-chip on algorithm, architecture, and circuit levels. All developed methods will be integrated in the commercial design flow and will be applied to industrial level SoC, which will be fabricated on silicon.

• “Position RA-DD2018”

Qualifications:
-MSc. degree in Microelectronics or Computer Engineering.
-2-4 years digital IC design experience (candidate “must” have previous tapout experience).
-Professional level in HDL is required (VHDL and/or Verilog).
-Hands-on experience with ASIC/FPGA design flow including simulation, synthesis, verification, timing analysis, physical design, and chip signoff.
-Experience in asynchronous logic, cryptoprocessor design, HW S&T is preferable.
-Experience in PCB design, board assembly, and chip testing is a plus.
-Knowledge of Matlab and Simulink is a plus.
-Knowledge of programming (C or C ++) is a plus.
-Hands-on experience with scripting (C Shell, TCL, Perl, … etc).
-Excellent English communication skills is required.

Potential candidates are welcome to send a cover letter, and their resumes with an email subject [RA-DD2018] to: Dr. Eslam Tawfik (~LOGIN--ad26ccbd38ba2e10613071ccada2863?ou[dot]edu )

• “Position RA-CAD2018”

Qualifications:
-MSc. degree in Computer Science, or Computer Engineering.
-2-4 years experience in developing CAD methods for IC design.
-Deep understanding of algorithm design, data structures, and efficient programming.
-Professional level in HDL is required (VHDL and/or Verilog).
-Hands-on experience with ASIC/FPGA design flow including simulation, synthesis, verification, timing analysis, physical design, and chip signoff.
-Knowledge of Matlab is required.
-Excellent programming skills (C or C ++ ) is required.
-Hands-on experience with scripting (C Shell, TCL, Perl, … etc) is required.
-Excellent English communication skills is required.

Potential candidates are welcome to send a cover letter, and their resumes with an email subject [RA-CAD2018] to: Dr. Eslam Tawfik (--LOGIN--ad26ccbd38ba2e10613071ccada28637osu[d]edu )

- “Position PD-DD2018”

Qualifications:
-PhD degree in Microelectronics, or Computer Engineering.
-5 years digital IC design experience (candidate “must” have multiple tapout experience).
-Professional level in HDL is required (VHDL and/or Verilog).
-Excellent experience with ASIC/FPGA design flow including simulation, synthesis, verification, timing analysis, physical design, and chip signoff.
-Deep understanding of algorithm design, data structures, and efficient programming.
-Experience in ML, deep-learning, neuromorphic computing is preferable.
-Experience in asynchronous logic, cryptoprocessor design, HW & T is a plus.
-Experience in PCB design, board assembly, and chip testing is a plus.
-Knowledge of Matlab and Simulink is a plus.
-Experience in programming (C or C ++ ) is a plus.
-Hands-on experience with scripting (C Shell, TCL, Perl, … etc).
-Excellent English communication skills is required.

Potential candidates are welcome to send a cover letter, and their resumes with an email subject [PD-DD2018] to: Dr. Eslam Tawfik (--LOGIN--ad26ccbd38ba2e10613071ccada28637osu[d]edu )

- Postdoc position in information theory and security at University of California, Santa Cruz (USA). Received: 22/10/2018.

The Department of Electrical and Computer Engineering at University of California, Santa Cruz is looking for a Postdoc in the area of Information theory and security. The selected candidate will work at the intersection of information theory, coding, and security. The candidate is expected to participate in development and implementation of new protocols. The candidate will work with UCSC Professor Hamid Sadjadpour. The appointment will be for a period of 12 months, renewable by mutual agreement for another year.

Qualifications:
Applicants should have a Ph.D degree, a strong foundation in information theory, strong publication record and excellent oral and written communication skills. Prior experience in the area is information theoretic security is desirable. Priority is given to candidates who are currently living in the US.

How To Apply:
Interested applicants should submit a current CV and a one-page research statement to --LOGIN--c836739239afsc73355d0de6033555567Huo[d]jusc[d]edu. The candidate should mention the relevant courses and papers related to this position in the research statement. Names of two references should be provided. Only short-listed candidates will be notified. Application closes when the position is filled.

- Open researcher position in database, data mining, machine learning, and transfer learning at Osaka University (Japan). Received: 22/10/2018, deadline: 25/12/2018.

Open researcher position at the Graduate School of Information Science and Technology, Osaka University

Graduate School of Information Science and Technology, Osaka university has an open researcher position related to developing persona model construction and transfer learning techniques, which aims at encouraging e-commerce so that cross domain customer transfer is enabled, and we are now inviting applications to a postdoctoral researcher position.

This recruitment relates to the project “Persona model construction from multi-and diverse-sources,” and a successful applicant is expected to work with Professor Takahiro Hara, Osaka University, and other project members including those from KDDI Research Inc. and Nagoya University.

Please refer to the following web page for detailed research topics of Professor Hara's research laboratory.
http://www-mmde.ist.osaka-u.ac.jp/

For more detailed information please contact Professor Takahiro Hara (--LOGIN--d31f4df8f8a01c7367fbb5d304634bd1ist[dot]osaka-u[dot]ac[dot]jp ).

--Job Description--
Research activities in database, data mining, machine learning, and transfer learning.

--Qualifications--
[Essential]
(1) Ph.D. or equivalent in related field/s is required.
(2) Basic knowledge in machine learning is required.

[Desirable]
(1) Research and/or system management experience of machine learning.
--Number of Positions--
One

--Employment Conditions--
Position: Specially Appointed Researcher (Full-time)
Affiliation: Multimedia Data Engineering Course, Department of Multimedia Engineering, Graduate School of Information Science and Technology
Work Location: Osaka University Suita Campus (1-5 Yamadaoka, Suita-city, Osaka)
Starting Date: On March 1st or as soon as possible thereafter (negotiable)
Probation period: 6 months

The term of Employment: From the starting date to March 31st, 2020
*Contract may be renewed after expiration of the term of contract (based on achievement of the applicant and extension of the project term)
Maximum term is no longer than 5 years.

Salary and Benefits: Based on the *Salary Regulations for National University Corporation Osaka University Limited Term Staff Subject to Annual Salary System (Specially appointed Staff, etc.),
Remuneration: 5,049,600 JPY or higher per annum. (Monthly payments of one-twelfth of annual salary)

Commuting allowance, housing allowance, dependent allowance, retirement allowance and bonus will not be provided.

Employment Form: The discretionary Labor System, Special Work Type is applied. (Deemed working hours: 8 hours a day)
*Based on the "Regulations Pertaining to Working Hours, Holidays and Leave for National University Corporation Osaka University Limited Term Staff,

Insurance: Successful candidate will join National Public Service Personnel Mutual Aid Associations, Employment Insurance and Workers Accident Compensation Insurance.

--Application Documents--
Applications must be written in English or Japanese and include the following.
1. Curriculum Vitae (photo attached) *Please use the university form available at the following website.
2. List of research achievements and academic publications (Please classify publications into categories such as original paper, international conference paper, book, review paper, tutorial paper, patent and so on.)
3. 10 separate prints of your major original papers (Photocopies acceptable)
4. Name and outline of prizes or awards you have received.
5. Names and contact information of the referee

--Sending Address and Contact Information--
[By postal mail] Takahiro Hara (Prof.)
Multimedia Data Engineering Course, Graduate School of Information Science and Technology, Osaka University
A201, 1-5 Yamadaoka, Suita-city, Osaka, 565-0871, JAPAN
*Write "Application for faculty position 'Specially Appointed Researcher (Full-time) in Multimedia Data Engineering Course‘" on the envelope in red ink.
*Send application documents by registered mail.
*Submitted application documents will not be returned.

[By e-mail]
hara@ist.osaka-u.ac.jp
*Write "Application for faculty position 'Specially Appointed Researcher (Full-time) in Multimedia Data Engineering Course‘" in the subject line.

--Application deadline--
December 25th, 2018. (Japan time) or until the position is filled.

--Selection Process--
After documents have been reviewed, only shortlisted candidates will be interviewed (by Skype).
*Shortlisted candidates will be notified within two weeks. Please note that unsuccessful applicants after initial screening will not be contacted.
*Travel and accommodation fees necessary for interviews are to be covered by the applicant.

--Additional Information--
Please refer to Work Regulations for National University Corporation Osaka University Limited Term Staff’ and other regulations for work and other related conditions.

Information in this document is subject to change. Please confirm details at interview.

Osaka University is promoting gender equality. (Reference: http://www.osaka-u.ac.jp/en/guide/diversity)
Graduate School of Information Science and Technology enforces independent support. We encourage applications from women.

--Use of personal data--
All personal data received will only be used for the purpose of application screening and hiring procedure.

◊ Two PhD positions at the University of Manchester (UK). Received: 17/10/2018, deadline: 09/11/2018.

Two open PhD positions in the Advanced Processors Group (APT) at the University of Manchester.

The first PhD project will be on sandboxing of reconfigurable accelerators as needed for multi tenancy (multi user) execution on FPGAs.
Background: we are building an FPGA operating system that supports hosting of multiple reconfigurable accelerators in a very flexible manner (see our paper “Resource Elastic Virtualization for FPGAs using OpenCL”).
However, to fully support this approach, modules have to be securely encapsulated. We will therefore build countermeasures against several existing (and maybe new) FPGA security threats.
The second project is about designing the architecture and mostly the development of the design tools for a new memristor-based FPGA fabric (that will be taped out).

This project will be part of the EPSRC-funded programme grant FORTE (Functional Oxide Reconfigurable Technologies); see: http://gow.epsrc.ac.uk/NGBOViewGrant.aspx?GrantRef=EP/R024642/1

Applications are accepted until November 9th. and interviewing starts as soon as applications arrive, so early applications are encouraged.

The APT Group in Manchester hosts several FPGA and hardware related projects spanning from full custom chips over boards to large systems to applications. We are therefore providing candidates with an environment where they can experience the full picture of hardware design.

For questions, please contact: Dirk Koch, --LOGIN--1260408b9365e14a34e4ac0de13c3e2amanchester[dot]ac[dot]uk

- Tenure Track Faculty Opening in Computer and Information Technology at Purdue University (USA). Received: 17/10/2018.

Purdue University's Department of Computer and Information Technology invites applications for tenure track faculty position at the rank of assistant professor for the West Lafayette campus beginning in August 2019. The department's mission focuses on teaching, research and industry/professional engagement. All candidates must have an earned PhD Degree in software development or a related field by the start of employment, candidates completing their PhD degree before Fall 2019 are encouraged to apply. Responsibilities include research, teaching undergraduate and graduate courses, and engagement with industry, the community, and colleagues within the computing discipline. Additional information about the department can be found at http://www.polytechnic.purdue.edu/CIT/

Purdue University Polytechnic Institute is committed to advancing diversity in all areas of faculty effort, including scholarship, instruction, and engagement. Candidates should address at least one of these areas in a cover letter, indicating how their past experiences, current interests and activities, and/or future goals could promote a work climate that values inclusion.

Application Instructions: Applications should include a cover letter, complete resume with the names and addresses of three references, and teaching and research statements. Optionally, applicants may include copies of relevant publications.

The application should be submitted to: http://purdue.taleo.net/careersection/wlfac/joblist.ftl. Screening of applications will begin December 1, 2018 and will continue until the position is filled. A background check is required for employment in this position.

Purdue University is an EOE/AA employer. All individuals, including minorities, women, individuals with disabilities, and veterans are encouraged to apply.

- Call for Professors in Computer Science and Engineering at Kyungpook National University (Korea). Received: 12/10/2018, closing date: 28/10/2018.

Kyungpook National University (KNU) is one of the top-level national government-supported universities in Korea. The School of Computer Science and Engineering (CSE) at KNU is home to specialized global talents that actively cope with the rapidly changing IT field. The school offers students challenges and experiences. http://computer.knu.ac.kr/eng/main/index.html

The school invites two tenure-track faculty positions, for applicants with foreign nationalities. Our school CSE has 32 tenure-track faculties, and among them, two have foreign nationalities. We plan to invite more foreign professors for our internationalized education and research programs.

Applications are sought for a tenure-track position in Computer Science and Engineering, at the rank of Assistant or Associate Professor, specifically in the area of computer science and engineering, including (but not limited to) Artificial Intelligence, Programming Languages, Compiler, Algorithms, Theoretical Computer Science, Data Mining, Bio-informatics, and Computer Security.

As a national university of Korea, the annual salary of a faculty member in tenured track is based on the Korean government salary system. In accordance with the system, new foreign professors have received an average salary of 64,000,000 KRW per year in the last two years. Nevertheless, the final salary will be determined along with applicant's overall academic performance. In addition to the annual salary, tenure-track professors can get incentives and extra financial supports, based on his/her achievements and research projects.

Responsibilities
- Teach computer science subjects at undergraduate and postgraduate levels.
- Supervise computer science PhD students and postdoctoral researchers.
- Obtain research grants from municipal, provincial and national funding bodies as well as the private sector.
- Conduct world-class research in computer science and allied areas.
- Disseminate research results in prestigious international journals and conferences.
- Perform administrative duties as required.

Qualifications
- The candidates must have a PhD in computer engineering or a closely related field.
- The candidates must have excellent written and verbal communication skills.
- Relevant industrial R&D experiences are positive factors.
- Experiences in Korea and Korean Language are optional, but also positive factors.
- The successful applicant should start before 1 March 2019, but an earlier start date is also possible.

Application requirements
Applications are to submit electronically through email, denoted at the end of this document. The application should contain:
- CV including information pertaining to the given qualifications
- List of publications with bibliographical references
- Copies of the Certificate of Research or Work Experience
- Copies of Transcripts and Degree Certificates (Bachelor, Master, and Ph.D.)
- A copy of the Ph.D. Thesis
- Research proposal for the first five years of employment (free style, 1 or 2 pages)
- Copies of your selected publications (at most 5 publications)
- Names and contact details of 3 references (name, relation to applicant, e-mail and telephone number)
- (Optional) TOPIK score, or proof of your Korean Language skill

Deadline to submit the application: 28 Oct 2018.
Contact Person:
Prof. Nakhoon Baek
Kyungpook National University
E-mail: --LOGIN--36484498d5ac83ab5d58598bd7af280@gmail.com, --LOGIN--d39b08b991632bc314f1bb13ac2a9d81kn[dot]ac[dot]kr

Contrato predoctoral en Neurociencia Cognitiva en la Universidad de Granada (Spain). Received: 11/10/2018, closing date: 29/10/2018.

El grupo de Neurociencia Cognitiva de la Universidad de Granada ofrece un contrato en el programa de Ayudas para contratos predoctorales para la formación de doctores 2018 (Antiguas FPI), asociada al proyecto PSI2017-88136-P: “Bases neurales de la consciencia perceptual y la consciencia fenomeno gica”, dirigido por la Dra. Ana B. Chica (https://blogs.ugr.es/attentionandconsciousness/). Estamos interesados en un candidato/a con perfil de ingeniería que se especializará en el análisis de señales de electroencefalografía (EEG) y resonancia magnética funcional (fMRI). Se trabajará en un equipo multidisciplinar formado por psicólogos/as, ingenieros/as, médicos/as, entre otros.

Los interesados/as pueden contactar con la investigadora principal del proyecto (Ana Chica, --LOGIN--cc3ac21c26936dd69b6731cc277c9cf3ugr[dot]es ).

Plazo de solicitudes: del 9 de octubre al 29 de octubre de 2018 a las 15.00h, ambos inclusive (hora peninsular española).

http://www.ciencia.gob.es/portal/site/MICINN/menuitem.dbc68b34d11ccbd52f9feb801432ea0/?vgnextoid=131955e25e01610VgnVCM1000001d04140aCRD&vgnextchannel=115222e98875610VgnVCM1000001d04140aCRD

Research Assistant/Associate working in PACE (Privacy-Aware Cloud Ecosystems) at Newcastle University, Newcastle Upon Tyne (UK). Received: 07/10/2018, closing date: 02/11/2018.

Faculty/Services: Faculty of Science, Agriculture & Engineering
Unit: Computing
Sub Unit: Computing
Staff Category: Research
Contract Type: Fixed Term
Hours of Work: Full Time
Location: Newcastle upon Tyne

Salary:
Research Assistant £27,831 to £29,515 per annum (PhD near completion)
Research Associate £30,395 to £39,610 per annum (PhD awarded)

Closing Date: 2 November 2018

You will design and develop privacy-aware prototypes to increase the “trust” of data providers (ordinary users) to the on-line service providers in handling and managing their data. To this end, the General Data Protection Regulation (GDPR) will be adapted to the prototypes to ensure that non-expert users can make informed decisions about their privacy and thereby give ‘informed consent’ to the use, sharing and re-purposing of their personal data.

You should have a strong publication record, have experience of designing, engineering and maintaining large-scale software projects, Knowledge of IoT, big data analytics and Experience in blockchain technology.

You will be integrated into an agile research team and should demonstrate the ability to write up research for publication in high peer-reviewed journals and conferences. Good oral and written communication, including presentation skills, and the ability to publish in relevant top peer-reviewed publications is essential along with a competency to conduct individual and collaborative research projects.

This post is fixed term for 24 months.

Informal enquiries can be made to Professor Rajiv Ranjan at --LOGIN--2c5253b2ed27c7a1115d2b6d57e3d529ncl[dot]ac[dot]uk

For more information about the faculty, please click here

The University holds a silver Athena SWAN award in recognition of our good employment practices for the advancement of gender equality, and the University holds the HR Excellence in Research award for our work to support the career development of our researchers. We are also a member of the Euroax network.

Newcastle University is committed to being a fully inclusive Global University which actively recruits, supports and retains staff from all sectors of society. We value diversity as well as celebrate, support and thrive on the contributions of all our employees and the community they represent. We are proud to be an equal opportunities employer and encourage applications from everybody, regardless of race, sex, ethnicity, religion, nationality, sexual orientation, age, disability, gender identity, marital status/civil partnership, pregnancy and maternity, as well as being open to flexible working practices.

Please be advised that due to a new minimum salary threshold of £30,000 per annum imposed by the UKVI, this post may not qualify for University sponsorship under Tier 2 of the points based system.

Associate professor position in Distributed Systems at University of Oslo (Norway). Received: 02/10/2018, closing date: 01/11/2018.

A position as associate professor in distributed systems is available with the research section of Networks and Distributed Systems at the University of Oslo.

The section is seeking a scientist with clear research visions, educational and administrative skills and abilities to establish and lead research projects.

The closing date for applications is November 1st, 2018.
How to apply is described in

HOST INSTITUTION AND ENVIRONMENT
The University of Oslo is Norway's largest and oldest institution of higher education. Founded in 1811, today the University of Oslo has approx. 30,000 students and 4,600 employees. Four Nobel Prize winners and two Turing award recipients indicate the quality of the research at the University in general and at the Department of Informatics in particular.

Norway has ben consistently ranked by the UN as having the highest standard of living in the world. It is also known for its unique scenic beauty. The work is in a smart futuristic building that has won multiple awards.

The research section of Networks and Distributed systems (ND) has nine full- and part-time faculty members. It encompasses research activities in most layers of distributed systems and networking, and has tight collaboration and synergy in the areas of information security and energy informatics. Within distributed systems, the ND section has been considering fundamental middleware techniques of self-adaptation, dependability, scalability, and data dissemination, with the objective of enhancing user experience and quality of services. The ND section has been applying these techniques to a large spectrum of application domains, such as blockchain technologies, IoT and sensor nets, cloud and fog computing, cyberphysical systems, multimedia systems, publish/subscribe, dark Internet, distributed machine learning, social networks, and others.

For example, we are considering software-defined infrastructures for big IoT data capable of both offline analysis and online control of massive scale data systems such as smart cities. In the blockchain domain, we are investigating both open and consortium blockchains, motivated by the application domains of sharing of medical data, energy markets, document storage, supply chains, and others. In the context of distributed machine learning, we are interested in scaling deep neural networks through parallelization and application of distributed algorithms.

SUITABLE BACKGROUND AND REQUIREMENTS
The candidate must have achieved associate professor level in informatics and have formal qualifications within the general area of distributed systems. At otherwise equal qualifications preference will be given to candidates with research applicable to energy informatics. While candidates at various levels of expertise within the range field of distributed systems will be considered, priority will be given to candidates with up to 10 years of experience after completion of PhD, who already have a scientific track record showing great promise, with an emphasis on the last five years.

The applicant should describe how his/her research and teaching is related to the current activity in the Networked and Distributed Systems section and at the department and how it may extend and complement it.

Many computer science projects at the department use combinations of simulations, experiments, development of prototypes and empirical analysis. While a wide span of research methods including complementary theoretical research is considered an advantage, the candidate is expected to have expertise in system building and empirical analysis in order to contribute to such projects.

The University of Oslo is a multi-lingual environment, where prior knowledge of Norwegian is not a prerequisite. However, the appointee is expected within two years to learn enough Norwegian to be able to participate actively in all functions the position may involve. Fluency in both written and spoken English is a prerequisite.

CONTACT INFORMATION
Further information on application requirements and on the application process is described in

Interested candidates can contact
Román Vitenberg (~LOGIN--14fl0159e97355ae8b683f104b12ff56ef[i]dot[j]uo[j]dot[n]o) or
Frank Eliassen (~LOGIN--89bc20c527934e11f6b92dd053d4e9beff[i]dot[j]uo[j]dot[n]o)

for further information.

Faculty Position in Pervasive Computing at Concordia University, Montreal (Canada). Received: 28/09/2018, closing date: 01/12/2018.

Ranked among the top ten engineering schools in Canada, the Gina Cody School of Engineering and Computer Science is home to over 10,000 engineering and computer science students and a faculty complement of 230 faculty members. The Gina Cody School of Engineering And Computer Science has about 4,500 graduate students enrolled in 35 graduate programs. The School's research profile continues to grow as it fosters multidisciplinary approaches to finding solutions to a broad range of societal challenges.

For more information on the School, please visit: http://concordia.ca/ginacody.

The Department of Electrical and Computer Engineering has a faculty complement of 44 professors active in various areas of electrical engineering and computer engineering. The department offers PhDs, master’s and bachelor's degrees in Computer Engineering, Electrical Engineering and Aerospace Engineering (Avionics). Concordia University attracts high quality domestic and international students in all its programs and has stable and strong enrollment at both the undergraduate and graduate levels.

More information on the Department is available at http://concordia.ca/ece.

The Department invites applications for a tenure-track position in Pervasive Computing. The appointment will be at the rank of Assistant Professor, but exceptional candidates at the Associate Professor level may also be considered. Applicants shall hold a PhD in a relevant Engineering discipline and shall possess teaching and research expertise in areas such as, but not limited to: real-time embedded systems, wearable computers, Internet of Things, sensor rich systems, platforms and software systems for pervasive computing, cloud computing for pervasive computing, wireless communication and networking for pervasive computing.

The successful candidate is expected to demonstrate a strong commitment to the supervision of MASc and PhD students, and to excellence in teaching at both the graduate and undergraduate levels. She/he will conduct independent scholarly research, attract strong external funding and demonstrate industrial application of their research activities.

Further information is available at: http://ece.concordia.ca/
We are specifically looking for young and upcoming researchers with domain experience in applications areas such as Blockchain, FinTech, Intelligent Robotics, Smart Service Productivity, Smart Manufacturing, Visualization, Healthcare, Hyperconnectivity & IoT and Security & Privacy.

Successful applicants will find NTU to be a stimulating environment conducive to professional growth and interdisciplinary collaborative research.

The University offers competitive remuneration package commensurate with qualifications and experience, and comprehensive benefits. For further information about the School of Computer Science and Engineering, please refer to www.scse.ntu.edu.sg.

To apply, please refer to the Guidelines for Submitting an Application for Faculty Appointment (http://www.ntu.edu.sg/ohr/career/submit-an-application/Pages/Faculty-Positions.aspx) and send your application to --LOGIN--8586fb8ada63bfdf05007db999019c436ntu[dot]edu[dot]sg . Electronic applications are preferred.

Review of applications will commence immediately. As such, applicants are strongly encouraged to apply early in order to be considered before all available positions are filled. Applicants must state which of the aforementioned areas they are applying for when submitting their applications. Only shortlisted candidates will be notified.

Closing Date: 1 Dec 2018

PhD position in Cognitive IoT/5G/6G Architectures and Beyond at the Norwegian University of Science and Technology (Norway). Received: 20/09/2018, deadline: 17/10/2018.

Available PhD position in Cognitive IoT/5G/6G Architectures and Beyond at the Norwegian University of Science and Technology (NTNU).

Deadline for applications: October 17th, 2018

Applicants should hold a master’s degree in computer science, communication and information technology or statistical machine learning. Proficiency of the English language will be required to work in a highly international research environment.

Research activities are to be carried out at the Department of Information Security and Communication Technology (IIK, Trondheim), and associated with NTNU IoT, QUAM and Norwegian Open AI Labs.

Full details available at:
http://www.jobbnorge.no/en/available-jobs/job/157826/phd-position-in-cognitive-iot-5g-6g-architectures-and-beyond

Several Research Positions in High Performance Deep Learning and Online Education at Hong Kong Baptist University (China). Received: 20/09/2018.

Our research group has several research positions in the areas of (i) High Performance Deep Learning; and (ii) Online Education.

Post-doctoral Research Fellow in High Performance Deep Learning

Applicants should possess a PhD Degree in computer science, computer engineering, or a related field, and sufficiently demonstrate abilities to conduct high-quality research in the area of high performance deep learning. Initial duration of this position is 12 months, and is renewable subject to satisfactory performance and mutual agreement.
(2) Research Assistant /Senior Research Assistant in High Performance Deep Learning

Applicants should possess a Bachelor Degree (or equivalent) in computer science, computer engineering, or a related field. R&D experience in any of the following areas is considered as a plus: (i) GPU computing, (ii) Deep learning, (iii) Network performance modelling. Initial duration of this position is 12 months, and is renewable subject to satisfactory performance and mutual agreement.

(3) Research Assistant /Senior Research Assistant in Online Education

Applicants should possess a Bachelor Degree in computer science, computer engineering, or a related field. Online-education system development experience is a plus. Initial duration of this position is 12 months, and is renewable subject to satisfactory performance and mutual agreement.

(4) Research Assistant /Senior Research Assistant in Online Education

Applicants should possess a Bachelor Degree (or equivalent) in education or psychology. Working experience in the education sector is a plus. Initial duration of this position is 12 months, and is renewable subject to satisfactory performance and mutual agreement.

We offer a competitive salary and benefits package, and Hong Kong practices a simple and low-rate tax system. Interested applicants are invited to send a CV to Prof. Xiaowen Chu by email (----LOGIN--c550439f221e42935a80c0d26abf1d16comp[dot]hkbu[dot]edu[dot]hk ). The position remains open until it is filled.

* Available PhD position in Blockchain Technology at the University of Oslo (Norway). Received: 17/09/2018, closing date: 07/10/2018.

A PhD position in blockchain technology is available with the group of Networks and Distributed Systems at the University of Oslo.

The area of the position is new distributed applications based on the blockchain paradigm, as well as extending the functionality of Bitcoin by providing distributed services atop its infrastructure:

http://heim.ifi.uio.no/~romanv/romanchain-phd-2018.html

The closing date for applications is October 7th, 2018.

HOST INSTITUTION AND ENVIRONMENT

The University of Oslo is Norway's largest and oldest institution of higher education. Founded in 1811, today the University of Oslo has approx.

30,000 students and 4,600 employees. Four Nobel Prize winners and two Turing award recipients indicate the quality of the research at the University in general and at the Department of Informatics in particular.

The students in our group won best paper and best demo awards at several conferences. Our alumni are employed by IBM Research, Google, Microsoft, Spotify, Schildstedt and highly reputable academic institutions in Europe.

Norway has been consistently ranked by the UN as having the highest standard of living in the world. It is also known for its unique scenic beauty.

The work is in a smart futuristic building that has won multiple awards.

SUITABLE BACKGROUND AND REQUIREMENTS

Applicants must have a degree in Computer Science, or in a related study, with excellent results. They must also be able to demonstrate interest in scientific research. The evaluation considers different aspects of excellence, as well as the personal drive and organizational skills. The ideal candidate for the positions will have strong background in distributed computing. Additional area-specific knowledge in distributed transactions, P2P, cryptography, or consistent data replication is beneficial but not required.

Master students may apply even if they have not yet completed their degree, but expect to do so before the position starts.

Knowledge of Norwegian is not a prerequisite for application. English is our working language for research. PhD students and postdocs in our group have been coming from more than 10 different countries. CONTACT INFORMATION

The application process is described in

https://www.jobbnorge.no/en/available-jobs/job/157195/phd-research-fellowship-in-blockchain

Interested candidates can contact Roman Vitenberg (----LOGIN--14f1059e8735ea8b683f104b12ff5f6ii[dot]uio[dot]no ) for further information.

* Project Manager (postdoctoral position) at Tampere University (Finland). Received: 17/09/2018, closing date: 11/10/2018.

Tampere University of Technology (soon to become Tampere University), Finland, is currently seeking a project and training manager at postdoctoral level for the recently accepted H2020 European Joint Doctorate program A-WEAR.

We are looking for candidates with experience in project management, a doctoral degree in a technical field relevant to the project with a good publication record, and good web-programming skills.

More information about the position and its requirements, and the link to the application form is found from Tampere University of Technology open positions page (www.tut.fi/openpositions -> Project Manager) or directly, through this link https://tut.rekryointi.com/paikat/?o=A_A&jid=43

* 1-year research position in imaging and machine learning at Biocruces Bizkaia, Bilbao (Spain). Received: 17/09/2018.

We are looking for an Engineer with skills in computer vision, programming and machine learning to collaborate in the research project entitled: “Study of the retina and the visual pathway by neuroimaging in genetic and aggressive idiopathic synucleinopathies as a model to identify phenotypes and prognostic biomarkers in idiopathic Parkinson's disease” (Principal Investigator: Ilgigo Gabilondo, MD, PhD) led by the Neurodegenerative Diseases Group. The data to analyse are multimodal and longitudinal, with a sample size bigger than N=100 and consisting in retina OCT, neuroimaging (structural, functional, tensor tensor and spectroscopy), neuropsychology and clinical scales.

The engineer also will work in close collaboration with the Computational Neuroimaging Group, led by Prof. Jesus M Cortes.
For further information about the groups, visit:
https://biocruesbizkaia.org/web/biocrues/bc5.01 and
https://biocruesbizkaia.org/bc5.08

Requirements:
- Bachelor of Engineering
- Good academic marks (average score higher than 7 over 10)
- Experience in computer vision, programming (jMatlab, Python and/or R), machine learning and other artificial intelligence techniques
- Experience with the magnetic resonance imaging analysis packages SPM, FSL and FreeSurfer
- Advanced English level (demonstrable with official degree – C1)
- Intention to develop a PhD in Biomedicine. The contract is for 1 year, but exists the possibility to extend it to finalize the PhD

Working conditions:
- Duration: 12 months (extendable)
- Full working day (100%) (1592 annual hours)
- Estimated annual gross salary: 23,004 euros per year
- Expected start date: As soon as possible

Interested candidates send 2 reference letters and updated CV to --LOGIN--ef104b662035aadfcd256ec0ebc970a3gmail[dot]com and/or --LOGIN--b368f5474c9fab211576e9eef76279cgmail[dot]com

- 2 Faculty Positions in Computer Engineering, AI and Robotics at University of Hong Kong (China). Received: 16/09/2018, deadline: 30/09/2018.

We have 2 tenure-track associate/assistant professor positions opened at University of Hong Kong in the general area of Computer Engineering, AI and Robotics. We offer highly competitive startup package and invite you to join our rapidly expanding Faculty. For more information, please see the advertisement at:
http://jobs.hku.hk/jd.php?id=201801142
and
http://jobs.hku.hk/jd.php?id=201801237

Deadline to apply is Sep 30, 2018.

- PhD Candidate in Database Forensics at the University of Zagreb (Croatia). Received: 14/09/2018, deadline: 05/10/2018.

As part of the Erasmus + project ISSES (Information Security Services Education in Serbia), the Croatian Science Foundation (HRZZ) sponsors a 4-year PhD position, which should be recruited at the Artificial Intelligence Laboratory and the Centre for Biometrics, Computer Forensics and Privacy at the University of Zagreb. The contest has been announced in the national gazette (in Croatian, under no. 1) here https://narodne-novine.nn.hr/clanci/oglas/o8256553.html. The deadline for application is October 5th, 2018, but please inquire earlier since appropriate documentation has to be submitted.

The subject of the doctoral dissertation is related to the field of database forensics, and the selected candidate will enroll in the doctoral study “Information Science” at the Faculty of Organization and Informatics.

Terms & Conditions
- Completed graduate university or integral studies (master level) of information technology or related disciplines.
- The total average of grades at the previous levels of study (undergraduate, graduate or integrated study) sufficient for enrollment in doctoral studies.

Desirable knowledge and skills:
- Enthusiasm in the field of databases, data science and open-source, and the desire for learning and research.
- Basics of computer forensics and forensic analysis.
- Basics of relational database management systems (e.g. PostgreSQL, MySQL, SQLite, Oracle, MS SQL and/or others).
- Basics of the so-called management system. NoSQL databases (e.g. MongoDB, CouchDB, Redis, Teradata, ZODB and/or others).
- Good knowledge of programming languages for data processing (e.g. SQL, Datalog, map/reduction techniques, XQuery and/or other), programming languages for graphical interfaces (Visual Basic and/or Gambas etc.) and scripting languages for working with data (Python, NodeJS and/or R).
- Programming Code Versions Management Tool (Git).

Interested candidates can contact --LOGIN--021732969a4da65f29b2294ee47157bbf[dot]hr for further information on the project and work place.

- Contrato FPI para la realización de tesis doctoral en el campo de tecnología para la mejora del aprendizaje, en la Universidad de Valladolid (Spain). Received: 14/09/2018.

El grupo de investigación GSIC-EMIC de la Universidad de Valladolid busca candidatos para un contrato FPI (Formación de Personal Investigador) asociado al proyecto “Analítica del aprendizaje para mejorar el diseño y la orquestación en entornos inteligentes de aprendizaje escalables y ubicuo, enriquecidos con Internet de las cosas (SmartLET)” (TIN2017-85179-C3-2-R) de la convocatoria 2017 del Programa Estatal de I +D+i Orientada a los Retos de la Sociedad. El candidato seleccionado trabajará con el objetivo de completar una tesis doctoral antes de la finalización del contrato en el contexto de investigación definido por el proyecto dentro del Programa de Doctorado en Tecnologías de la Información y las Telecomunicaciones o el Programa de Doctorado en Informática.

Duración y salario
El contrato tiene una duración de 4 años y ofrece un salario bruto de 16.422€ al año que subiría a 19.000€ al año en caso de que se obtenga el título de doctor antes de finalizar el tercer año de contrato. También incluye 6.250€ para pagar estancias cortas en otros centros de investigación así como la matrícula del programa de doctorado.
Requisitos y perfil
Se buscan candidatos con un título de Máster de cualquiera de las ramas de ingeniería de telecomunicación o de ingeniería informática. También son adecuados los candidatos con un título de Grado en cualquiera de esas mismas ramas y un título de Máster de Investigación. Además, se valorarán especialmente el expediente académico, el dominio del inglés y la experiencia relacionada con la temática del proyecto.

Solicitud
Está previsto que la convocatoria oficial de contratos FPI por parte del Ministerio de Ciencia, Innovación y Universidades tenga lugar en septiembre de 2018. Se espera que, una vez publicada la convocatoria oficial, los candidatos tengan que presentar su solicitud ante el Ministerio, siendo los investigadores principales del proyecto los encargados de llevar a cabo un informe de evaluación científico-técnica de cada una de las candidaturas. Los candidatos interesados en el contrato ofrecido por el grupo de investigación GSIC-EMIC pueden enviar ya su currículum vitae actualizado y el expediente académico a los investigadores principales del proyecto, Yannis Dimitriadis ( Yannis.Dimitriadis@ucr.ac.uk) y Miguel Bote (M.Bote@ucr.ac.uk). Los candidatos interesados en el contrato ofrecido por el grupo de investigación GSIC-EMIC pueden enviar ya su currículum vitae actualizado y el expediente académico a los investigadores principales del proyecto, Yannis Dimitriadis (Yannis.Dimitriadis@ucr.ac.uk) y Miguel Bote (M.Bote@ucr.ac.uk).

ProfessorPositions -Faculty Positions in Higher Education. Received: 12/09/2018.

ProfessorPositions

Computer Science
• Chair of Computer Science (W3), University of Münster, Germany
• Full Professor of Computer Science, University of Münster, Germany
• Lecturer in Computer Graphics, University of Auckland, New Zealand
• Lecturer in Computer Vision, University of Auckland, New Zealand
• Lecturer in Data Science, University of Auckland, New Zealand
• Lecturer in Security & Crime Science, University College London, United Kingdom
• Research Associate in Design of a Transceiver Circuit for Wireline Communication at 28 NM Technology, University of Manchester, United Kingdom

• Research Associate Position in Electronic Engineering -Sensor Fusion at the University of Bedfordshire and Harrod Sport Ltd (UK). Received: 07/09/2018, deadline: 30/09/2018.

University of Bedfordshire and Harrod Sport Ltd hire a Research Associate under the Knowledge Transfer Partnership (KTP) program funded by Innovate UK agency in the area of integrating a sensor fusion system and developing and testing algorithms for ball localization in various sports.

Contract type: Full time, 30 months fixed-term
Salary: GBP 27,500 (depending on experience and qualifications)
Location: Lowestoft, Suffolk, UK

Harrod Sport Limited is the UK’s leading manufacturer of football goal posts, hockey goals, tennis posts and general sports ground equipment.

In partnership with the University of Bedfordshire (under the Knowledge Transfer Partnership scheme), Harrod Sport Limited are looking for an enthusiastic recent graduate (max 5 years since graduation) to help deliver a project and to take a new innovative product to market. This is fantastic opportunity for an ambitious graduate to launch a career in industry with the support of company and academic mentors over a period of 30 months.

You will be responsible for the project management, involving the development of an optimised combination of sensors (including ultrasonic, infrared, cameras and laser sensors) that will estimate in real-time the data related to the ball velocity and interception point location between the ball and football goal bar plane. You will be also responsible for developing a microcontroller system to support the sensors and to enable real-time wireless data transmission to a mobile device user application.

Essential Criteria
• MSc in Electronic/Electrical Engineering or in subjects very closely related to this
• Knowledge of a broad range of microcontroller and sensor technologies
• Strong knowledge of electronics assembly processes and test methodologies
• Understanding of software design for microcontrollers
• Excellent verbal and written communication skills with the ability to understand complex technical information and convey this into easily understood instructions
• Ability to work to deadlines while demonstrating a track record for innovation and creativity
• Strong command of English language with the ability to accurately interpret complex standards
• The ability to contribute to a team effort whilst being a self-starter, highly motivated, and willing to take the project lead
• The ability to demonstrate a logical approach to problem solving and methodical, investigate-oriented and inquisitive mind

Desirable Criteria
• Demonstrable experience in developing electronic systems
• Demonstrable experience in previous assembly of sensor and microprocessor systems
• Demonstrable experience in optimised algorithm development and the field of signal processing
• Previous work in teams and on problem-solving in electronic engineering

Interested applicants should email the detailed CV and cover letter by 30th September 17.00 UK time to Samantha Griffin (Samantha.Griffin@ucr.ac.uk). For any queries regarding the post, please refer to Dr Yannis Dimitriadis (Yannis.Dimitriadis@ucr.ac.uk) and Dr Miguel Bote (M.Bote@ucr.ac.uk).

ProfessorPositions -Faculty Positions in Higher Education. Received: 06/09/2018.

ProfessorPositions

Computer Science
• Tenure-track assistant Professor of Simulation, University of Copenhagen, Denmark
• Chair of Computer Science (W3), University of Münster, Germany
• Full Professor of Computer Science, University of Münster, Germany
• Chair in Machine Learning (Full or Associate professor), University of Amsterdam, Netherlands
PostDoc Position on Programmable Systems for Intelligence in Automobiles (ECSEL/PRYSTINE Project)

Deadline: Oct 31, 2018
Duration: 36 months

To apply: email your CV, publication list, (links to) MSc and PhD reports to: --LOGIN--c67e66c1354c527c9c9e630f0bc5b569tudelft[dot]nl

TUD/EEMCS/CAS in ECSEL Project PRYSTINE

The Technische Universiteit Delft (TUD) is a world-class university ranked 20th in engineering and technology in the 2017 Times Higher Education World University Rankings. Established in 1842, it is the largest and oldest institute of technology in the Netherlands. TUD's eight faculties are home to over 20,000 students and some 900 academic staff and cover the entire spectrum of technology, with a combined science, engineering and design focus. The Circuit and Systems lab (CAS) conducts research on efficient digital hardware design for a broad range of computing use-cases with varying power-performance-cost targets. Outcomes of CAS's activities typically include hardware prototypes, design, and simulation frameworks, as well as virtual prototypes.

Context and Mission

Applications are invited for a PostDoc to conduct research and development on the creation of highly-efficient programmable hardware for sensor fusion and AI in the context of the ECSEL-funded project Programmable Systems for Intelligence in Automobiles “PRYSTINE” (ADAS). The focus will be on the design of programmable compute hardware to enable automatic driving functions, across two application targets: data fusion for robust perception, and acceleration of AI frameworks for decision making. The PostDoc will investigate the applicability of neuromorphic computing architectures, and programmable hardware fabrics, within PRYSTINE’s envelope of objectives: robust perception & dependable embedded control at reduced cost and power-consumption.

Key Duties

Contribute to the research and application of ML/AI decision making software and hardware. This includes the design a programmable hardware system that can efficiently fuse heterogeneous data streams from disparate sensors and data sources, and produce a robust, consolidated data stream that can be used as the basis for object recognition, and scenario assessment. The system will also accelerate communications firmware for interfacing with remote sensors. These efforts will span the flow from architecture exploration, down to system-level design and prototyping, as well as physical design. The hardware neural networks realized either as a full-custom/semi-custom VLSI implementation or within a programmable fabric. Finally, to facilitate efficient and robust multi-sensor data fusion across disparate sources, investigate the hardware acceleration of communication stacks within the programmable system.

Qualification required

This position requires a doctoral degree (or relevant experience) in electronic engineering, computer engineering, or computer science field; or (equivalently) 3 years of expertise on the topics relevant to the position. A successful candidate has significant experience in VLSI digital and mixed signal system design, circuit design, and knowledge of deep neural networks, machine learning, and analytical modeling. A proven record of working and collaborating on larger research and development projects, and a record of developing high-quality results. This position includes close interaction with a team of people to design optimal solutions. Excellent skills in spoken and written English are required. Skills include digital hardware design and HDL languages (Verilog, VHDL), experience with digital hardware testing and simulation, programming languages knowledge (C, C++).

More information about PRYSTINE at: http://ens.ewi.tudelft.nl/Research/project.php? id=120

Post-Doctoral Researcher Position in Middleware and IoT at the Technical University of Berlin (Germany). Received: 04/09/2018.

GT-ARC gGmbH in Berlin, Germany, is currently seeking a post-doctoral researcher to lead its R&D activities in the areas of Internet of Things and distributed middleware technologies.

GT-ARC is an affiliated research institute of the Technical University of Berlin, which is also co-located and works closely with the Distributed Artificial Intelligence Laboratory (DAI-Labor) at TU Berlin, under the leadership of Prof. Dr. Sahin Albyarik. The advertised position will have active involvement in research and development activities at both GT-ARC and DAI-Labor.

The selected candidate will supervise a team of four to five researchers (Ph.D. students) at GT-ARC /TU Berlin and should be able to conduct both theoretical and applied research towards the design and development of novel IoT solutions.

More details on this and two other open positions can be found at http://www.gt-arc.com/jobs/, together with the application guidelines.

Ver anteriores

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