Ofertas de Trabajo de Otras Entidades

Oficinas Web UGR

Ofertas diversas

- Post-Doctoral Researcher Position in Middleware and IoT at the Technical University of Berlin (Germany). Received: 04/09/2018.

GT-ARC gGmbH in Berlin, Germany, is currently seeking a post-doctoral researcher to lead its R&D activities in the areas of Internet of Things and distributed middleware technologies.

GT-ARC is an affiliated research institute of the Technical University of Berlin, which is also co-located and works closely with the Distributed Artificial Intelligence Laboratory (DAI-Labor) at TU Berlin, under the leadership of Prof. Dr. Sahin Albayrak. The advertised position will have active involvement in research and development activities at both GT-ARC and DAI-Labor.

The selected candidate will supervise a team of four to five researchers (Ph.D. students) at GT-ARC/TU Berlin and should be able to conduct both theoretical and applied research towards the design and development of novel IoT solutions.

More details on this and two other open positions can be found at http://www.gt-arc.com/jobs/, together with the application guidelines.

- Research Associate Position in Electronic Engineering -Sensor Fusion at the University of Bedfordshire, Lowestoft (UK). Received: 15/08/2018.

University of Bedfordshire and Harrod Sport Ltd hire a Research Associate under the Knowledge Transfer Partnership (KTP) program funded by Innovate UK agency in the area of integrating a sensor fusion system and developing and testing algorithms for ball localization in various sports.

Contract type: Full time, 30 months fixed-term
Salary: GBP 27,500 (depending on experience and qualifications)
Location: Lowestoft, Suffolk, UK

Harrod Sport Limited is the UK’s leading manufacturer of football goal posts, hockey goals, tennis posts and general sports ground equipment.

In partnership with the University of Bedfordshire (under the Knowledge Transfer Partnership scheme), Harrod Sport Limited are looking for an enthusiastic recent graduate (max 5 years since graduation) to help deliver a project and to take a new innovative product to market. This is fantastic opportunity for an ambitious graduate to launch a career in industry with the support of company and academic mentors over a period of 30 months.

You will be responsible for the project management, involving the development of an optimised combination of sensors (including ultrasonic, infrared, cameras and laser sensors) that will estimate in real-time the data related to the ball velocity and interception point location between the ball and football goal bar plane. You will be also responsible for developing a microcontroller system to support the sensors and to enable real-time wireless data transmission to a mobile device user application.

Essential Criteria
- MSc in Electronic/Electrical Engineering or in subjects very closely related to this
- Knowledge of a broad range of microcontroller and sensor technologies
- Strong knowledge of electronics assembly processes and test methodologies
- Understanding of software design for microcontrollers
- Excellent verbal and written communication skills with the ability to understand complex technical information and convey this into easily understood instructions
- Ability to work to deadlines while demonstrating a track record for innovation and creativity
- Strong command of English language with the ability to accurately interpret complex standards
- The ability to contribute to a team effort whilst being a self-starter, highly motivated, and willing to take the project lead
- The ability to demonstrate a logical approach to problem solving and methodical, investigate-oriented and inquisitive mind
Desirable Criteria
• Demonstrable experience in developing electronic systems
• Demonstrable experience in previous assembly of sensor and microprocessor systems
• Demonstrable experience in optimised algorithm development and the field of signal processing
• Previous work in teams and on problem-solving in electronic engineering

Interested applicants should email the detailed CV and cover letter by 30th September 17.00 UK time to Samantha Griffin (LOGIN--7a11fb51cd65b73233330082cd7271bed[dot]ac[dot]uk ). For any queries regarding the post, please refer to Dr Vladan Velisavljevic (LOGIN--b99e6d343d00d0b454cc749c263f044bed[dot]ac[dot]uk ).

20 post-doc/researcher positions at Tallinn University of Technology (Estonia). Received: 27/07/2018.

Tallinn University of Technology is looking for candidates to 20 new post-doc/researcher positions

What we offer:
- A competitive net salary in a range of 20,000 - 30,000 EUR per year
- Location in Tallinn, which is one of the fastest-growing IT hubs in Europe and home to The European Agency for the operational management of large-scale IT systems (eu-LISA) as well as the NATO Cyber Defence Centre of Excellence
- A young and internationally active faculty with rapid and efficient university e-management, allowing you to concentrate on the essential

Who we are:
Estonia is the homeland of pioneering ICT solutions which include Skype, e-identity, e-health, and e-governance. Tallinn University of Technology is the flagship academic institution of Estonian ICT research, engineering and technology education. Our international faculty and staff form a unique and multidisciplinary team of technological, natural, exact, economic and health sciences. The university has 12,000 students and 2,000 employees and is located in the Estonian capital city of Tallinn.

Whom we are looking for:
Estonian ICT industry and academia are establishing three new research groups in the areas of Trustworthy Software Technologies, Hardware Security and Trust, Internet of Intelligent Things. In addition we are reinforcing our existing research groups with people with AI/machine learning, data science/big data and human-robot interaction background.

The School of Information Technologies at Tallinn University of Technology is looking for candidates for 20 new post-doc/researcher positions with a proven research track record, ability and interest to collaborate across disciplines, willingness to cooperate with industry and the public sector.

Successful candidates in the area of Hardware Security and Trust are expected to perform basic and applied research and teaching related to HW security and trust with a focus on tools and methodologies for secure hardware design, hardware architectures for cryptography, side channel attacks and countermeasures, isolation in virtualized systems, ensuring configuration integrity of network devices, among others.

Successful candidates in the area of Internet of Intelligent Things are expected to perform basic and applied research and teaching related to IoT with a focus on embedded artificial intelligence and machine learning, sensor fusion, design of dependable intelligent devices under IoT application constraints, intelligent networked control systems and IoT communication protocols, among others.

Successful candidates in the area of Trustworthy Software Technologies are expected to perform basic and applied research and teaching related to trustworthy software technologies with a focus on theories, methods and tools for program analysis, verification, program transformations and generation, program synthesis, programming languages, functional programming, refinement/dependent types, software contracts, theorem proving and proof assistants, certified software, processes of building trust in software, economics of trust, among others.

How to apply:
Applications should include a cover letter, documents proving that the candidate holds the required academic degree (PhD or an equivalent qualification) and has acquired the required education, a curriculum vitae and a list of publications, a statement of future research and teaching interests and other documents considered to be important by the applicant.

Documents should be sent by e-mail to --LOGIN--c8f257227163abaa1b437c964235488ettu[dot]ee

Application cut-off date: Last date of each month until all positions are fulfilled

Further information regarding the professorship, please contact Professor Gert Jervan, Dean of the School of IT, --LOGIN--3649e240d75a4cd8b0241471e5d1e9a44ttu[dot]ee


Further information about working and living in Estonia can be found: https://www.euraxess.ee/ and http://www.workinestonia.com/

PhD Positions in Computer Science at IMT School for Advanced Studies Lucca (Italy). Received: 25/07/2018, deadline: 03/08/2018.

36 POSITIONS PhD SCHOLARSHIPS AT IMT LUCCA

We are pleased to announce the IMT School for Advanced Studies Lucca's PhD Program on Systems Science Specifically we signal its Computer Science and Systems Engineering track (https://www.imtlucca.it/phd/2018-19/computer-science).

Applications for prospective PhD students in Computer Science with an interest in any of the following topics are particularly welcome:

- Modeling, analysis, and verification of concurrent and distributed systems
- Quantitative methods for the evaluation of computing systems
- Cybersecurity

The scholarship consists of a grant amounting to €15,300 gross/year for three years, plus free accommodation and board at the IMT Campus in the center of Lucca.
Most IMT School PhD Graduates have reached prominent roles in academia, governmental institutions, public and private companies or professions across the globe.

APPLY HERE: https://www.imtlucca.it/phd

N.B.: Candidates can apply by August 3, 2018 if they expect to obtain their (minimum) 4-year undergraduate degree by October 31st, 2018

- Un appel a la candidature pour 400 bourses d'études Canadienne au titre de l'année académique 2018-2019, Université Laval, Québec (Canada). Received: 24/07/2018.

Par l'intermédiaire de la direction régional des bourses étrangères canadienne, le secrétariat d'état a l'étude et a la recherche de l'Université Laval lance un appel a la candidature pour 400 bourses d'études Canadienne au titre de l'année académique 2018-2019. Ces bourses sont destinées aux ressortissants des pays de la catégorie A (pays industrialisés européens, et extra-européens) et ceux des pays de la catégorie B (pays en développement, du tiers monde et extra-européens) elles doivent leur permettre de poursuivre leurs études, de parfaire leurs connaissances pour les travaux de recherches dans les domaines auxquels l'Université Laval accorde une attention particulière.

SPÉCIFICITÉ DE LA BOURSE:
- L'Université Laval entend faciliter l'immigration aux personnes désirées de Poursuivre leurs études et d'obtenir des diplômes d'état canadienne.
- Les candidats retenus au terme de la sélection de candidatures seront insérés outre leur étude dans les secteurs sensibles de la vie économique et sociale du Canada: (santé, droit, diplomatie, communication, finance, énergie, industrie, transport, agriculture...). Cette option de l'Université Laval vise a donner une aptitude professionnelle aux boursiers pour pouvoir travailler s'ils le désirent au Canada a la fin de leur formation.

DURÉE DE LA BOURSE:
Les bourses couvrent la période d'un cycle de formation ou au maximum six (06) semestres voir plus.

FRAIS DE VOYAGE:
Les billets d'avion aller-retour (Provenance – Canada/Québec), sont pris en charge par la direction régional des bourses étrangères canadienne.
Conditions préalables a la candidature en règle générale, les candidats aux bourses étrangères Canadienne doivent:
- Avoir au maximum 18 ans a 64 ans;
- Comprendre et parler correctement l'une des langues d'enseignement au Canada (Anglais, Français, Espagnol, Allemand, Italien);
- Avoir un diplôme équivalent au brevet d'étude de premier cycle d'enseignement, au baccalauréat ou au brevet d'aptitude professionnelle des pays de l'union européen.

PROCÉDURE DE SÉLECTION:
Retirer auprès de la commission fédérale des bourses étrangères Canadienne(CFBEC) le formulaire de demande de bourse via leur adresse E-mail: --LOGIN--ab1500d9a304bcd4477a752cf3ce952fsecretary[@]net

- La commission fédérale des bourses étrangères fera étudier votre dossier par la représentation déléguée de votre zone et catégorie de pays.
- Les candidats retenus recevront une attestation du secrétariat d'état a l'étude et a la recherche pour notification de la bourse. Les candidats désirés de participer aux bourses d'études 2018-2019 doivent écrire a cet e-mail pour retirer le formulaire a ce mail: --LOGIN--ab1500d9a304bcd4477a752cf3ce952fsecretary[@]net

- Research position in digital communications at CentraleSupélec, Rennes (France). Received: 16/07/2018, closing date: 09/2018.

Open Research Position

“Design and Implementation of New Signal Waveforms and Processing Techniques for a Lower Energy Consumption”

We are offering a full-time research scientist (postdoctoral/experienced engineer) position in the area of digital communications with a particular emphasis on adaptive radio access systems (single carrier and multi-carrier) and implementation on USRP platforms. Proposed research work has received a French government support granted to the CominLabs excellence laboratory and managed by the National Research Agency in the “Investing for the Future” program under reference ANR-10-LABX-07-01

Areas of interest:

We are looking for candidates with experience and knowledge on: digital communication systems more specifically on physical layer aspects related to the design and implementation of transmission and reception blocks, and skills with experience on hardware implementation on USRP platforms. Skills with experience using DSP and FPGAs with knowledge on C++ and/or Python programming may be very advantageous. Besides the applicant must:

1) Have excellent oral and writing in English.
2) Have EE engineering with experience on embedded electronic disciplines or a Ph.D. degree in EE.
3) Have a strong digital communications background.
4) Have already demonstrated research performance and capabilities by publications and patents.
For no French educated candidates, French speaking and/or writing is not required but may be advantageous.

The work of the candidate will consists in
1. Comprehension and mastery of proposed new modulation scheme for low energy consumption.
2. Develop proposed modulator, with appropriate precoding scheme using USRP platforms.
3. Develop the corresponding demodulator including the synchronization/equalization and the channel estimation stage, using USRP platform.
4. The overall proposed communication system must be tested under real situation.

Employment: The research grant is awarded for 18 months and its yearly amount is about €15,300. The position will take place at the IETR (Institute of Electronic and telecommunications of Rennes) in the SCEE (Signal, Communication & Embedded Electronics) research group of CentraleSupélec engineering school in campus of Rennes (France). Expected starting is: 1st January 2019.
Application: Interested candidates should contact: Jacques Palicot --LOGIN--1f22e51dbbc3f36ca52c8ad316d966f2centralesupelec[dot]fr , Carlos Fauzi Bader --LOGIN--a70935c07e0d7f278118c1f66dca5847asupelec[dot]fr , and Yves Louêt --LOGIN--37585557b88c632774edcf6edc2e190fcentralesupelec[dot]fr by sending a detailed CV, graduate transcript(s), representative publications, statement of research experience and interests, and two references.

We may contact interesting candidates once we receive the application. Therefore, please submit your application as early as possible. Application deadline: September 2019.

- Postdoc Position for Dense Reconfigurable Fabric Development at the University of Manchester (UK). Received: 12/07/2018, closing date: 06/08/2018.

The Advanced Processor Technologies group (APT) at the University of Manchester has an open post for a postdoc position: https://www.jobs.manchester.ac.uk/displayjob.aspx?jobid=15673 (Closing date: 06/08/2018)

The candidate will work within the EPSRC-funded project FORTE (Functional Oxide Reconfigurable Technologies); see: http://gow.epsrc.ac.uk/NGBOViewGrant.aspx?GrantRef=EP/R024642/1

FORTE is a large 5.5 year project involving material science (Southampton), analogue reconfiguration (Imperial College) and digital reconfiguration (Manchester).

The Manchester team will essentially develop (and tape-out) FPGAs that use memristor technology. We are consequently looking for a postdoc providing strong expertise in at least two of the following fields:

- FPGA architectures
- Development of FPGA design tools (e.g., experience with VTR - Verilog to routing)
- VLSI design
- Full custom CMOS design
- EDA tool programming (e.g., validation tools)
- Experience with IC design automation tools (e.g. multi-vendor flows combining Cadence and Synopsys tools)

For questions, please contact: Dirk Koch, --LOGIN--81245700f24acec531f358edac7c5e507manchester[dot]ac[dot]uk


La institución Canon Foundation Europe ofrece cada año 15 becas de investigación para promover el intercambio cultural internacional y el desarrollo de las relaciones científicas entre Europa y Japón.

Estas becas de investigación están destinadas a personas con un doctorado, o excepcionalmente, a aquellas que hayan obtenido un máster en los últimos 10 años en cualquier área de conocimiento. Pueden acceder a ellas tanto investigadores europeos como japoneses. Los ciudadanos europeos a los que les sean concedidas las ayudas deberán investigar en Japón, así como los ciudadanos japoneses en Europa.

La duración de estas ayudas se establecen en un mínimo de 3 meses hasta un máximo de 12 meses y su cuantía podrá variar entre 22.500 y 27.500 Euros.

El plazo para solicitar estas becas finaliza el día 15 de septiembre de 2018 y debe realizarse de manera online.

A continuación, adjuntamos una serie de enlaces para más información:

- Más información:
  https://www.canonfoundation.org/programmes/research-fellowships/
- Registro online:
  https://forms.canonfoundation.org/register.php
- Procedimiento solicitud de las becas:
  https://forms.canonfoundation.org/how-to-apply.php?_ga=2.190335016.2040606269.1530010101-1436997844.1530010101

Esperamos que esta información sea de su interés. Si necesita cualquier información al respecto, no dude en contactarnos. Estamos a su disposición en la Oficina de Proyectos Internacionales (OFPI):

Oficina de Proyectos Internacionales
Unidad de Promoción y Asesoramiento
Vicerrectorado de Investigación y Transferencia -Universidad de Granada.
C/Gran Vía de Colón, 48, 2ª Planta. 18071
+34 958 24 27 01
--LOGIN--b5fed3283675f2830e46607893907fugr[dot]es

- 10 plazas postdoctorales para investigadores excelentes con experiencia en el ámbito de la innovación y la transferencia en la Universidad de Granada (Spain). Received: 04/07/2018, deadline: 28/09/2018.

La Universidad de Granada convoca 10 plazas postdoctorales para investigadores excelentes con experiencia en el ámbito de la innovación y la transferencia.

Los candidatos a estas becas deberán cumplir los criterios de elegibilidad: https://athenea3i.ugg.es/?page_id=23

Los contratos serán por 3 años y las retribuciones brutas mensuales son de hasta 4.000€ y complementos de 1.500€, dependiendo de diversos aspectos tales como la situación familiar o aspectos derivados de la ejecución del proyecto (formación, estancias, etc).

Los investigadores contratados se incorporarán a equipos multidisciplinares para la realización de proyectos de investigación, innovación y desarrollo integrados en la Universidad de Granada, debiendo disponer de un supervisor de dicha institución.
Además, cada investigador tendrá la oportunidad de realizar una estancia voluntaria en una institución diferente de la UGR, con una duración de entre 3 y 9 meses (entre el mes 10 y 18 del proyecto) en el ámbito académico o empresarial, así como una estancia más breve con una duración de entre 3 y 6 meses en una institución no académica.

Las áreas de conocimiento son:
- Química y Ciencias Ambientales (ENV-CHE)
- Ciencias Económicas, Ciencias Sociales y Humanidades (ECO-SOC)
- Ingenierías y Ciencias de la Información (ENG)
- Ciencias de la Vida (LIFE)
- Física y Matemáticas (PHY-MAT)

El plazo de solicitud es del 4 de septiembre al 28 de septiembre de 2018, y se prevé la incorporación a la Universidad de Granada de julio a septiembre de 2019.

Toda la información relativa a la convocatoria y plazas convocadas (con todos los detalles de requisitos, actividad a realizar y retribución) puede consultarse en: https://athenea3i.ugr.es/

Para cualquier pregunta no dude en contactarnos: --LOGIN--0c863275083be300ac77c4543fb9403ugr[dot]es

- Two Postdoc Positions in programming languages, architecture, and VLSI at Cornell University (USA). Received: 02/07/2018.

We have two openings for postdoctoral researchers in areas related to programming languages, architecture, and VLSI at the Computer Systems Laboratory in Cornell University.

Postdoctoral Researcher Opportunity (1)

Project: Continuous Reconfiguration of Polymorphic Hardware
Location: Cornell University, Ithaca, NY
Advisers: Christopher Batten, Adrian Sampson, Zhiru Zhang
Duration: 1-2 years starting fall 2018

The Computer Systems Laboratory at Cornell seeks a postdoctoral researcher to help launch a new project to design a new kind of continuously reconfigurable machine. The project spans computer architecture, compilers, and programming languages research.

* The Team
The postdoc will work with the research groups of Professors Christopher Batten, Adrian Sampson, and Zhiru Zhang in the departments of Electrical and Computer Engineering and Computer Science. The postdoc will have opportunities to mentor PhD, masters, and undergraduate researchers in these groups. The project also includes research groups at the University of Washington.

* The Project
While one-off ASICs offer leaps in computational efficiency even in face of slowed returns from Moore's law, they sacrifice flexibility and programmability. We are designing a new kind of reconfigurable architecture based on a programmable memory system and configurable spatial compute fabric. The system is designed for high-frequency reconfiguration based on shifting application demands. It includes an HBM2 memory interface and CGRA-like spatial array of RISC-V cores with a reconfigurable interconnect. The project aims to approach ASIC-like efficiency by continuously optimizing the system's organization to specialize the compute datapath and exploit the HBM2 memory's high bandwidth and low latency. We plan to tape out prototype hardware and build a realistic compiler toolchain to target it.

* Background
We are seeking recent PhDs in CS or ECE with expertise in architecture and/or compilers. Researchers in one area or the other will fit well; researchers whose interests span hardware and software are ideal. Prior work on FPGAs, CGRAs, spatial architectures, program synthesis, and JIT compilers is all relevant. Good candidates will be “builders” and have a track record of releasing or deploying real systems.

* The Job
The position starts in the fall semester of 2018 and lasts for one or two years, depending on the candidate’s preference. The postdoc will work with the PIs and students to define the research direction, build the system, release open-source hardware and software, and write papers for top-tier architecture and PL conferences. This is a leadership role; there will be opportunities to mentor students and to give talks at companies and other universities.

* Diversity
We take diversity and inclusion seriously. Cornell is a recognized employer and educator valuing AA/EEO, Protected Veterans, and Individuals with Disabilities.

* How to Apply
Send your CV via email to --LOGIN--0360fd09e526aa05274dc30d517ac3f3bcornell[dot]edu , --LOGIN--0c85f251604dbca122cd8ae47a9533bc[dot]cornell[dot]edu , and/or --LOGIN--a3458b054aad2a10dc29c6eac6bcorneill[dot]edu

Postdoctoral Researcher Opportunity (2)

Project: Flexible On-Chip Network Generator for Cache-Coherent Memory Systems
Location: Cornell University, Ithaca, NY
Advisers: Christopher Batten
Duration: 1-2 years starting fall 2018

The Computer Systems Laboratory at Cornell seeks a postdoctoral researcher to help launch a new project to develop an open-source on-chip network generator for cache-coherent memory systems. The project spans computer architecture and VLSI design.

* The Team
The postdoc will work within the research group of Professor Christopher Batten in the department of Electrical and Computer Engineering. The
postdoc will have opportunities to mentor PhD, masters, and undergraduate researchers in these groups. The project also includes researchers at Princeton University.

* The Project
There is increasing excitement about open-source hardware but a lack of high-quality open-source IP available for use in new system-on-chip designs. This is particularly true with respect to scalable cache-coherent memory systems. We are aiming to develop and release a new open-source framework capable of flexibly generating on-chip networks specifically designed for use in scalable cache-coherent memory systems. This framework will be based on our prior work on PyMTL, a Python-based hardware modeling framework well-suited to generating both the on-chip networks and the associated test harnesses from high-level descriptions. There is also an option to lead a small VLSI test chip in the later stage of the project.

* Background
We are seeking recent PhDs in CS or ECE with expertise in architecture and/or VLSI. Researchers in one area or the other will fit well; researchers whose interests span architecture and VLSI are ideal. Prior work on on-chip networks is relevant but not required. Good candidates will have a track record of releasing open-source projects.

* The Job
The position starts in the fall semester of 2018 and lasts for one or two years, depending on the candidate’s preference. The postdoc will work with the PI and students to define the research direction, develop the framework, release the framework as an open-source project, and write papers for relevant conferences. This is a leadership role; there will be opportunities to mentor students and to give talks at companies and other universities.

* Diversity
We take diversity and inclusion seriously. Cornell is a recognized employer and educator valuing AA/EEO, Protected Veterans, and Individuals with Disabilities.

* How to Apply
Send your CV via email to: LOGIN--0360fa9e526aa05274dcc30d517acf3bcornell[dot]edu

- 2 PhD & 1 Postdoc positions in heterogeneous systems at Mälardalen University (Sweden). Received: 02/07/2018, deadline: 18/08/2018.

We are hiring two PhD-students and one postdoc at Mälardalen University (Sweden):

The overall goal of the recently funded project HERO (Heterogeneous systems: software-hardware integration) is to provide a framework that enables model-based development of optimized parallel software, automatic mapping of software to heterogeneous hardware platforms, and provision of automatic hardware acceleration for the developed software. The project will be carried out in cooperation with 5 Swedish industrial partners (Alten, Arcticus, Enea, Unibap, and Volvo).

HERO is divided in three distinct sub-projects (SP):
SP1: Model-based development and resource analysis of parallel software,
SP2: Pre-runtime code-level resource analysis, and
SP3: Automatic generation of hardware accelerators.

The first PhD student position will focus on modeling and analysis issues, linking SP1 and SP2. The Second position will focus on analysis, design and synthesis issues linking SP2 and SP3. The postdoc position will focus on hardware/software co-design for deep learning acceleration in SP3.

Instructions for applying: http://www.mdh.se/hogskolan/jobb/lediga-jobb-1.103104

The deadline for applying is 2018-08-18.

Contact:
Masoud Daneshfalab, Assoc. Professor
LOGIN--8e9924792ac041b192e9803c92012397mdh[dot]se

- Ver anteriores

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